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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/059,644  
Filing Date ..... April 13, 1998  
Inventor ..... Pai-Hung Pan  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2822  
Examiner ..... M. Trinh  
Attorney's Docket No. .... MI22-898  
Title: Semiconductor Processing Methods of Forming a Conductive Gate and Line

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the reference listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: Aug. 8, 2002

  
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